Q.P. Code: 16EC5501														R16
Reg. No:														
SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR (AUTONOMOUS) M.Tech I Year I Semester (R16) Regular Examinations January 2017 MICRO CONTROLLERS & INTERFACING (Common to CS, PE & ES)														
(For Students admitted in 2016 only) Time: 3 hours Max. Marks (Answer all Five Units 5 X 12 =60 Marks)														
Q.1	a. b.	UNIT-IDraw and explain the architecture of 8051 microcontroller with neat diagram.7Write short notes on different addressing modes in 8051 with examples?5												
Q.2	a. b.	OR Explain in brief about Timer in different modes.												7M 5M
Q.3	a. b.	Explain the Motorola 68HC11 microcontroller features. 4 Draw and explain the block diagram of PIC 16C74 microcontroller. 8												4M 8M
Q.4	a. b.	Explain in detail about Serial Communication Interface in Motorola 68HC11 Microcontroller Explain interrupts handing in <u>PIC 16C74</u> microcontroller.												la 6M 6M
Q.5	a. b.	Explain in brief about ATMEL external memory interfacing g Describe in detail about. (i) Timers (ii) Watchdog												5M 7M
Q.6	a. b.	OR Explain about PWM pulse generation using micro controller Give a brief note about ISP & IAP features											8M 4M	
Q.7	a. b.	Explain the 5-stage pipelining process of ARM Processor5Explain the following instructions of ARM processor5(i) TSTEQ r2, #5(ii) CMP r0, r1(iii) BICEQ7												5M 7M
Q.8	a. b.	Explain the complete register organization of ARM processor 8N List the various data transfer instruction supported for serial and parallel communication in ARM processor? 4N												8M id 4M
Q.9	a. b.	Explain in detail about interrupt vectors & priority. 7N Explain the various interrupt handling mechanisms 5N OR												
Q.10	a. b.	Discu List th	ss the ne app	e com olicatio	plete ons of	desig micr	n of ty ocont	ypical rollers ID ***	embe s in In	edded dustri	l syste al cor	em ntrol		8M 4M